

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

<b>Title of Invention</b>	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS																																																																																														
Submision Type : Utility Patent Filing Application Number: 10/707071  EFS ID: 50888 Server Response: <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 25%;">Confirmation Code</th> <th style="width: 75%;">Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>1070</td> </tr> <tr> <td>IGEN77</td> <td>Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either &lt;us-access-code&gt; not found in fee-xml OR actual access code not provided in the fucntion call.</td> </tr> </tbody> </table>				Confirmation Code	Message	ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application	ICON1	1070	IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fucntion call.																																																																																				
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First Named Applicant:	Darren Anand																																																																																														
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